

OLIFF & BERRIDGE, PLC  
Telephone: (703) 836-6400  
Facsimile: (703) 836-2787



CUSTOMER NUMBER 25944

**PATENT APPLICATION**

Attorney Docket No.: 117849

**AMENDMENT TRANSMITTAL**

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: T. QUACH

Filed: November 21, 2003

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT  
AND METHOD OF MANUFACTURING A TRANSISTOR

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

Entitlement to small entity status is hereby asserted.  
 Small entity status of this application has been established.

Any additional claim fees have been calculated as shown below:

	(Column 1)	(Column 2)	(Column 3)	SMALL ENTITY	OTHER THAN A SMALL ENTITY
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADD'L FEE
TOTAL CLAIMS	*25 MINUS	**20	=5	x 25	\$
INDEP CLAIMS	* MINUS	***3	=	x 100	\$
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM					+ 180 \$
					\$

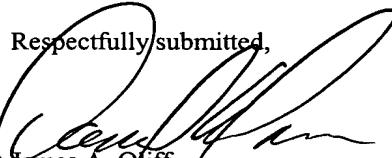
\* If the entry in Column 1 is less than the entry in Column 2, write "0" in Column 3.

\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.

\*\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space.

The "Highest Number Previously Paid For" in this space (Total or Independent) is the highest number found from the equivalent box in Column 1 of a prior Amendment or the number of claims originally filed.

Check No. 172111 in the amount of \$250.00 is attached. The Commissioner is hereby authorized to charge any other fees that may be required to complete this filing, or to credit any overpayment, to Deposit Account No. 15-0461. Two duplicate copies of this sheet are attached.

Respectfully submitted,  
  
James A. Oliff  
Registration No. 27,075

Daniel A. Tanner, III  
Registration No. 54,734



**PATENT APPLICATION**

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: T. QUACH

Filed: November 21, 2003

Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

**AMENDMENT UNDER 37 C.F.R. §1.114**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In consideration of the Request for Continued Examination filed herewith, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims;

**Remarks.**